

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Peter Korger NOV 30 2005
Robert Mod



Serial No. : 10/007,245

Group Art Unit : 2631

Filed : October 22, 2001

Examiner : Flanagan, Krista M.

For : Data Ready Indicator Between
Different Clock domain

Atty Docket : 1496.00173 / 01-284

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

Nov. 30, 2005
Date


Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 11/30/2005

Respectfully submitted,



Henry Groth

Reg. No. 39,696